Sp9907 — FPGA Board with High-speed 8chADC/4chDAC (Stratix)

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Introduction

A FPGA board designed for evaluation of high-speed signal processing, such as wireless communication, etc. By default, it is equipped with Altera FPGA(StratixIV), high-speed ADC/DAC, USB controller, SODIMM connector.

Features

- Altera StratixIV is deployed
  The following devices are selectable
  - ■EP4SGX180KF40
  - ■EP4SGX230KF40
  - ■EP4SGX290KF40
  - ■EP4SGX360KF40
  - ■EP4SGX530KH40
- ADC/DAC
  - ■SMA connector (differential)
  - ■AD converter 12bit 1Gsps 8channel
  - ■DA converter 14bit 2.4Gsps 4channel
- Clock
  - ■25.000MHz oscillator
  - ■150.000MHz oscillator
  - ■SMA input
  - ■On-board PLL

- Configuration
  - ■Serial configuration device
  - ■JTAG download port
- Memory
  - ■DDR3SDRAM 204pin SODIMM (Max 4GByte)
- Interface
  - ■USB2.0 B-connector(x1)
  - ■SMA connector for Transceiver
    TX/RX 16pair
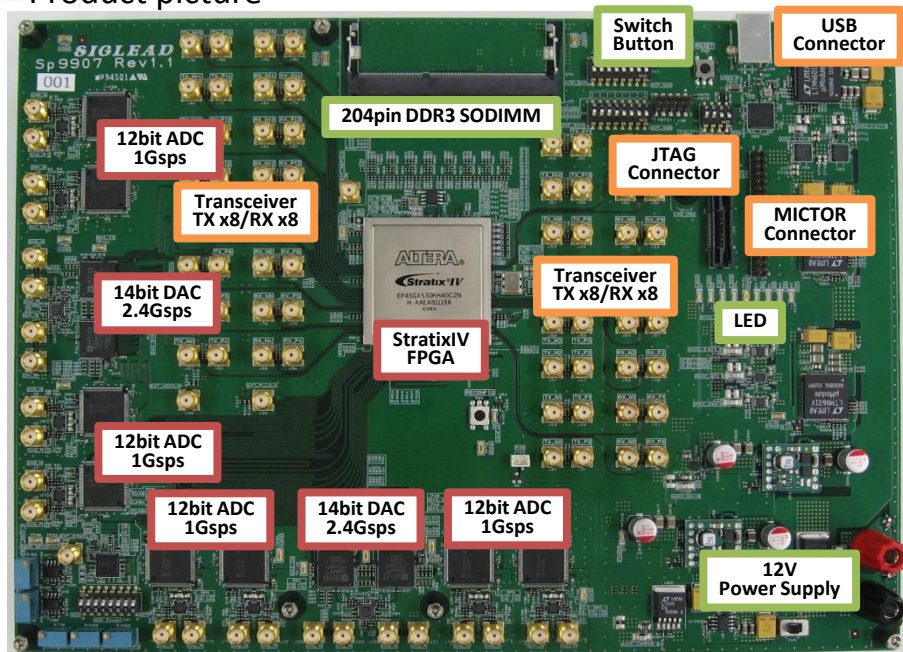 - ■MICTOR connector for logic analyzer
    (32pins for user)
- Universal user interface
  - ■DIP switch (x16)
  - ■Push button (x1)
  - ■LED (x8)
- 12V Switching power supply
- Size 210mm X 297mm

Product picture

Applications

- Wireless communication
- Others, application for handling high speed signal

Customization

Possible to meet various demands such as changing conversion rate, resolution and channel number of ADC/DAC etc.

[Customization example]
- 3Gsps 8bit ADC
- 200Msps 14bit ADC 4channel
- 200Msps 16bit DAC 4channel

The content might change without a previous notice due to the improvement.

Please contact us for further works such as customization and peripheral circuit design

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